



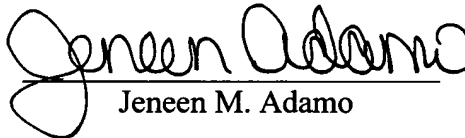
PATENTS
112055-0074U
17732-67080.00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of:)	
Jianhong Ju et al.)	
)	Examiner: Not yet assigned
Serial No.: 10/720,568)	
)	
Filed: November 24, 2003)	Art Unit: Not yet assigned
)	
For: FAILSAFE FOR DIFFERENTIAL)	
CIRCUIT BASED ON CURRENT)	
SENSE SCHEME)	
		Cesari and McKenna, LLP
		88 Black Falcon Avenue
		Boston, MA 02210
		March 9, 2004

CERTIFICATE OF MAILING

I hereby certify that the following Information-Disclosure Statement is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 9, 2004.


Jeneen M. Adamo

Commissioner for Patents
P.O. Box 1450
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Sir:

INFORMATION-DISCLOSURE STATEMENT

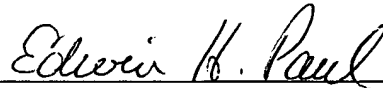
In keeping with the duty of candor and good faith owed to the Patent and Trade-mark Office, Applicants wish to bring to the Examiner's attention the references listed on the accompanying form PTO-1449. A copy of each listed reference is enclosed.

PATENTS
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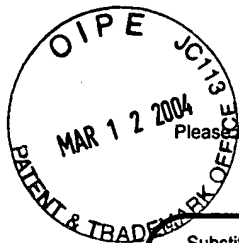
To the extent required by 37 C.F.R. §1.98(a)(3), Applicants have described what they consider to be the relevance of any foreign-language reference. The Office may find additionally relevant material in these or other references.

Please charge any additional fee occasioned by this paper to our Deposit Account No. 03-1237.

Respectfully submitted,

A handwritten signature in cursive script, reading "Edwin H. Paul", is written over a horizontal line.

Edwin H. Paul
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Please type a plus sign (+) inside this box →



PTO/SB/08A (08-00)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

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Complete if Known

Application Number	10/720,568
Filing Date	November 24, 2003
First Named Inventor	Jianhong Ju et al.
Group Art Unit	2836
Examiner Name	Not yet assigned
Attorney Docket Number	112055-0074U

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1.	TEXAS INSTRUMENTS INCORPORATED, SN65LVDS32B, SN65LVDT32B, SN65LVDS3486B, SN65LVDT3486B, SN65LVDDS9637B, SN65LVDT9637B HIGH-SPEED DIFFERENTIAL RECEIVERS, October 2000 SLLS440A Product Reference, Revised May 2001	
	2.	MAXIM, Low-Jitter, 800Mbps, 10-Port LVDS Repeaters with 100Ω Drive, Application Notes for the MAX9153/MAX9154, Revised October 2001	
	3.	GINGERICH, KEVIN, Wired-Logic Signaling With M-LVDS: High Performance Linear-Interface, Application Report SLLA119 for Texas Instruments, dated October 2002	

Examiner
Signature

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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